

NOTE: You may often encounter the **test eax,eax** instruction. Note that this is NOT like the cmp instruction. The register eax is not compared to itself like it happens with the CMP. The TEST instruction performs bitwise AND between two values. The instruction sets the flag ZERO if the result of the bitwise AND is 0 (hence, a possible jz instruction takes the branch). Since the bitwise and is performed between the register eax and itself, the only way to set up the bitwise AND to zero is **that eax is equal to zero**.

Hence, the jz branch is taken through test eax,eax only if eax = 0.